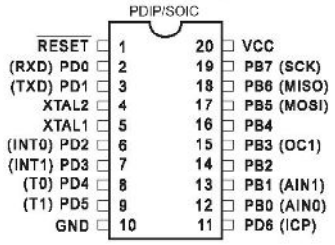
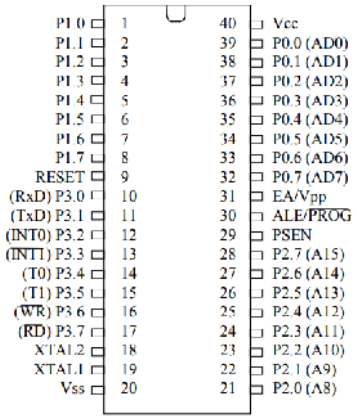


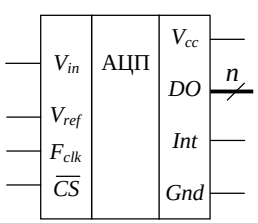
Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
SREG	I	T	H	S	V	N	Z	C
SPL	SP7	SP6	SP5	SP4	SP3	SP2	SP1	SP0
GIMSK	INT1	INT0	–	–	–	–	–	–
GIFR	INTF1	INTF0						
TIMSK	TOIE1	OCIE1A	–	–	TICIE1	–	TOIE0	–
TIFR	TOV1	OCF1A	–	–	ICF1	–	TOV0	–
MCUCR	–	–	SE	SM	ISC11	ISC10	ISC01	ISC00
TCCR0	–	–	–	–	–	CS02	CS01	CS00
TCNT0	Timer/Counter0 (8 Bits)							
TCCR1A	COM1A1	COM1A0	–	–	–	–	PWM11	PWM10
TCCR1B	ICNC1	ICES1	.	–	CTC1	CS12	CS11	CS10
TCNT1H	Timer/Counter1 – Counter Register High Byte							
TCNT1L	Timer/Counter1 – Counter Register Low Byte							
OCR1AH	Timer/Counter1 – Compare Register High Byte							
OCR1AL	Timer/Counter1 – Compare Register Low Byte							
ICR1H	Timer/Counter1 – Input Capture Register High Byte							
ICR1L	Timer/Counter1 – Input Capture Register Low Byte							
WDTCR	–	–	–	WDTOE	WDE	WDP2	WDP1	WDP0
EEAR	–	EEPROM Address Register						
EEDR	EEPROM Data Register							
EECR	–	–	–	–	–	EEMWE	EEWE	EERE
PORTB	PORTB7	PORTB6	PORTB5	PORTB4	PORTB3	PORTB2	PORTB1	PORTB0
DDRB	DDB7	DDB6	DDB5	DDB4	DDB3	DDB2	DDB1	DDB0
PINB	PINB7	PINB6	PINB5	PINB4	PINB3	PINB2	PINB1	PINB0
PORTD	–	PORTD6	PORTD5	PORTD4	PORTD3	PORTD2	PORTD1	PORTD0
DDRD	–	DDD6	DDD5	DDD4	DDD3	DDD2	DDD1	DDD0
PIND	–	PIND6	PIND5	PIND4	PIND3	PIND2	PIND1	PIND0
UDR	UART I/O Data Register							
USR	RXC	TXC	UDRE	FE	OR	–	–	–
UCR	RXCIE	TXCIE	UDRIE	RXEN	TXEN	CHR9	RXB8	TXB8
UBRR	UART Baud Rate Register							
ACSR	ACD	–	ACO	ACI	ACIE	ACIC	ACIS1	ACIS0

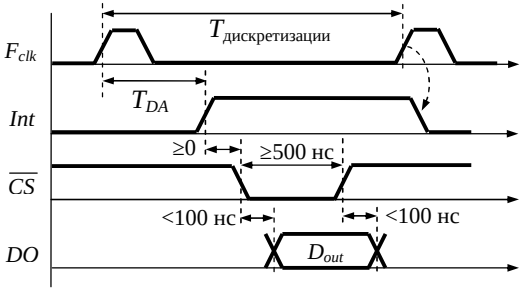
mov	add	cp	bst	rjmp	nop
ldi	adc	cpc	bld	rcall	sleep
in	adiw	cpi	bclr	ijmp	wdr
out	inc	tst	cset	icall	
lds	sub	and	sbi	ret	
sts	sbc	andi	cbi	reti	
ld	subi	cbr	lsl	brbc	
st	sbci	or	lsr	brbs	
ldd	sbiw	ori	rol	sbic	
std	dec	sbr	ror	sbis	
push	neg	eor	asr	cpse	
pop	com	clr			
lpm	mul	ser			
swap					

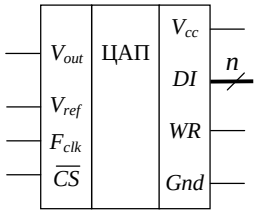


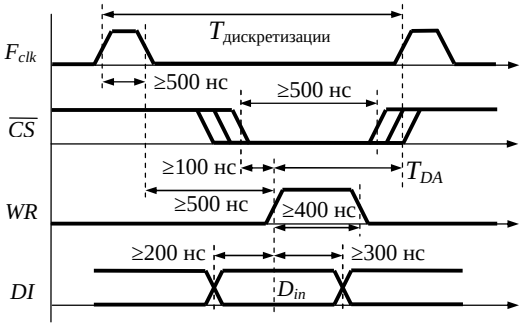
mov	add	anl	ljmp		
movc	addc	orl	ajmp		
movx	da	xrl	sjmp		
push	sbb	clr	jmp		
pop	inc	setb	jz/jnz		
xch	dec	cpl	jc/jnc		
xchd	mul ab	rl	jb/jnb		
swap	div ab	rlc	jbc		
		rr	djnz		
nop		rrc	cjne		
			lcall		
			acall		
			ret		
			reti		

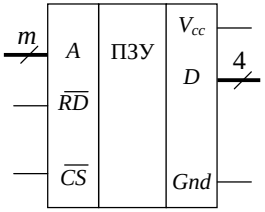
0F0h	*RG «B»*								
0E0h	*Acc*								
0D0h	*PSW*	*CY*	*AC*	*F0*	RS_1	RS_0	*OV*	*F1*	*P*
0B8h	*IP*	-	-	-	*PS*	PT_1	PX_1	PT_0	PX_0
0B0h	Порт 3								
0A8h	*IE*	*EA*	-	-	*ES*	ET_1	EX_1	ET_0	EX_0
0A0h	Порт 2								
099h	*SBUF*								
098h	*SCON*	SM_0	SM_1	SM_2	*Ren*	*Tb8*	*Rb8*	*TI*	*RI*
090h	Порт 1								
08Dh	*TH1*								
08Ch	*TH0*								
08Bh	*TL1*								
08Ah	*TL0*								
089h	*TMOD*	*Gate*	*C/T*	*M1*	*M0*	*Gate*	*C/T*	*M1*	*M0*
088h	*TCON*	TF_1	TR_1	TF_0	TR_0	IE_1	IT_1	IE_0	IT_0
087h	*PCON*	*smod*	-	-	-	GF_1	GF_0	*PD*	*IDL*
083h	*DPH*								
082h	*DPL*								
081h	*SP*								
080h	Порт 0								
07Fh 030h	ОЗУ								
02Fh 020h	128 бит								
01Fh 000h	*RG*-файл								



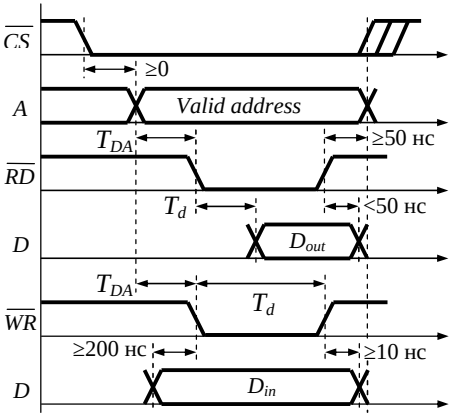


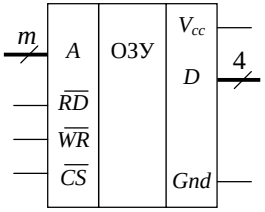






T_d ≥400 нс





T_d ≥800 нс